

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:)
 Shunpei YAMAZAKI et al.) Art Unit: 2813
 Serial No. 09/699,466) Examiner: Sarkar
 Filed: 10/31/2000)
 For: METHOD FOR FABRICATING A)
 SEMICONDUCTOR DEVICE)

**INFORMATION DISCLOSURE STATEMENT AND
 NOTICE OF RELATED APPLICATION**

Commissioner for Patents and Trademarks
 Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application.

Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a). The requisite fee of \$180.00 is also submitted herewith in accordance with 37 C.F.R. 1.97(c).

The Examiner's attention is directed to US application Serial No. 08/205,298 (hereafter '298).

Japanese Patent Laid-Open Nos. 06-314785 and 06-314787 both correspond to the '298 application, and disclose that edge parts of an island-shaped silicon domain show a tapered profile with an angle in a range of 20 to 60 degrees, as described in Embodiment 4 of the of '298 application.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

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